

**Notice of References Cited**

Application/Control No.

10/645,871

Applicant(s)/Patent Under  
Reexamination  
OGAWA ET AL.

Examiner

Ted T. Vo

Art Unit

2191

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,718,541 B2	04-2004	Ostanevich et al.	717/149
*	B	US-5,367,696	11-1994	Abe, Hitoshi	717/153
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Palem Krishna, "Code Optimization in Mordern Compiler", Department of Computer Science, NYU, pages: 1-29, 1998.
	V	Leung et al., "Run-time versus Compile-time Instruction Scheduling in Superscalar (RISC) Processors: Performance and Tradeoffs", CiteSeer, pages: 1-28, 1997.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.